

# LOW VOLTAGE OUTPUT DRIVE CIRCUIT

## Abstract of the Disclosure

5 A trigger circuit (22) having a depletion mode n-type transistor (32) and a depletion mode p-type transistor (34) operate by having each gate thereof driven by an independent source. When both transistors are on, the depletion mode n-type transistor (32) is  
10 driven by  $I_{s1}$  to  $V_{supply}$  and the depletion mode p-type transistor (34) is driven by  $I_{s2}$  to ground. When both transistors are off, a transistor (26) is switched on driving  $I_{s1}$  to ground, and a transistor (28) is switched on driving the gate of depletion mode p-type transistor  
15 (34) to  $V_{supply}$ . A linear regulator (50) using a depletion mode transistor pair (52, 54) with their gates thereof driven by separate sources provides a low voltage operation with minimal current leakage. One depletion mode transistor (52) is an n-type, and the second  
20 depletion mode transistor (54) is a p-type transistor.